Docket No.

209665US-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Seiichi MORI

SERIAL NO:

09/876,019

GAU:

2811

FILED:

June 8, 2001

EXAMINER:

Unknown

FOR:

SEMICONDUCTOR MEMORY INTEGRATED CIRCUIT AND ITS MANUFACTURING

METHOD

INFORMATION DISCLOSURE/RELATED CASE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

The applicant(s) wish to make of record the references listed on

A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

Attached is a list of applicant's pending applications which may be related to the present application. Accopy of and drawings of the pending application(s) is attached.

☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

□ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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LIST OF RELATED CASES

Docket Number	Serial or Patent No.	Filing or Issue Date	Status or Patentee
209665US2*	09/876,019	06/08/01	PENDING
214258US2S	09/956,986	09/21/01	PENDING
215615US2	09/984,599	10/30/01	PENDING



^{*}Present application; listed for information GJM/ae
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